

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Larry B. Li

Docket No.

50944.9900

Serial No.

09/738,127

Filing Date

December 15, 2000

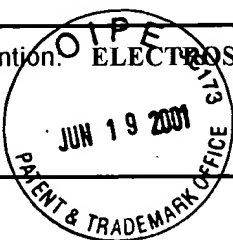
Examiner

TBD

Group Art Unit

2836

Invention: ELECTROSTATIC DISCHARGE PROTECTION CIRCUIT

**TO THE ASSISTANT COMMISSIONER FOR PATENTS:**

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	19 -	20 =	0 x	\$18.00	\$0.00
INDEP. CLAIMS	3 -	3 =	0 x	\$80.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No. _____ in the amount of _____
A duplicate copy of this sheet is enclosed.
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- ☒ Any additional filing fees required under 37 C.F.R. 1.16.
- ☒ Any patent application processing fees under 37 CFR 1.17.

Dated: June 19, 2001


Signature

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with the U.S. Postal Service as first
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20231.

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CC:



IN THE UNITED STATES PATENT AND
TRADEMARK OFFICE

PATENT

Applicants: Larry B. Li

Docket No.: 50944.9900

Serial No.: 09/738,127

Confirmation No.: 9821

Filing Date: December 15, 2000

Group Art Unit: 2836

Amended **ELECTROSTATIC DISCHARGE**
Title: **PROTECTION CIRCUIT**

Examiner: TBD

Handwritten notes:
#8
Procedurally
Ct. 6/15/01
72601

SECOND PRELIMINARY AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS

Box Missing Parts

Washington, D.C. 20231

Assistant Commissioner:

Please amend the above-captioned application by adding an Abstract of the Disclosure as follows and provided herewith on the following page:

ABSTRACT OF THE DISCLOSURE

The present invention is directed to a circuit that may be used to protect other circuits from Electrostatic Discharge ("ESD"). A circuit of the present invention comprises a transmission line that couples a source with a circuit that is being protected. A transistor, with an emitter, a collector, and a base, is coupled in reverse mode between an electrical ground and the circuit being protected. This circuit may be used in applications using high frequencies, such as 1 GHz or higher.

Respectfully submitted,

Date: June 19, 2001

By: 

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